



# USB 3.0 Technology: Comprehensive Guide to SuperSpeed USB

*By Donovan Anderson, Jay Trodden*

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## USB 3.0 Technology: Comprehensive Guide to SuperSpeed USB By Donovan Anderson, Jay Trodden

The third generation of USB increases transfer rates to 5.0 Gbits/s (SuperSpeed) and includes backward compatibility with all earlier USB specifications (1.1/2.0) and speeds (Low Speed, Full Speed, and High Speed) USB 3.0 SuperSpeed protocol also brings significant enhancements in the areas of device and system power conservation, error handling, and data flow control. SuperSpeed bus instances replace the USB broadcast bus model with directed (unicast) packets. Dual-simplex signaling enables asynchronous device notifications and simplifies link partner communication used in link flow control, packet acknowledgement and retry, and power management transitions. MindShare's books take the hard work out of deciphering the specs, and this one follows that tradition. MindShare's USB 3.0 Technology book provides a thorough description of the interface with numerous practical examples that illustrate the concepts. Written in a tutorial style, this book is ideal for anyone working with USB technologies.

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### **Editorial Review**

#### **Review**

A critical success factor in developing high speed serial data protocols is ramping up on these technologies quickly. We have found that MindShare books, like USB 3.0 Technology, allow engineers to learn new protocols more quickly and efficiently compared to reading the specifications. Mike Micheletti - USB Product Manager, Teledyne LeCroy --Mike Micheletti

#### **About the Author**

Donovan Anderson has worked with MindShare for over 21 years and has conducted courses on a wide range of topics including USB and has authored/coauthored 14 MindShare books including USB 2.0 System Architecture and PCI Express. Jay Trodden has been a computer hardware designer for many years and a MindShare instructor for nearly 14 years. His design and teaching experience includes work with many processors and a number of bus architectures, including PCI, PCI-X, PCI Express, HyperTransport, and USB. Jay co-authored MindShare's HyperTransport System Architecture book.

### **Users Review**

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